## **EAST Search History**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	6273	(transistor\$1 fet\$1) near3 (reverse adj bias\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/12 11:40
L2	0	L1 near3 ((programmable adj logic adj device\$1) PLD\$1 (logic adj chip))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/12 11:41
L3	4	L1 same ((programmable adj logic adj device\$1) PLD\$1 (logic adj chip))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/12 12:34
L4	43	((programmable adj logic adj device\$1) PLD\$1 (logic adj chip)) near3 (transistor\$1 fet\$1) near3 (used using)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/12 14:19
L5	4	("6777978" "6621325").pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/12 14:27
L6	2	"6144357".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/12 14:27
S1	545	transistor\$1 near5 ((programmable adj logic adj device\$1) PLD\$1)	USPAT	OR	OFF	2007/02/07 14:43
S2	10	S1 near5 bias\$3	USPAT	OR	OFF	2007/02/07 14:44
S3	1	S2 same (reverse forward)	USPAT	OR	OFF	2007/02/09 15:22
. <b>S</b> 4	15	(reverse adj bias\$3) near5 (low adj power)	USPAT	OR .	OFF	2007/02/07 13:34

## **EAST Search History**

S5	2	S4 near5 transistor\$1	USPAT	OR	OFF	2007/02/07 14:34
S6	2	S4 near5 (transistor\$1 fet\$1)	USPAT	OR	OFF	2007/02/07 14:34
S7	555	(fet\$1 transistor\$1) near5 ((programmable adj logic adj device\$1) PLD\$1)	USPAT	OR	OFF	2007/02/07 14:35
S8	0	S4 and S7	USPAT	OR	OFF	2007/02/07 14:35
S9 .	859	transistor\$1 near5 ((programmable adj logic adj device\$1) PLD\$1 fpga\$1 cpld\$1)	USPAT	OR .	OFF	2007/02/07 14:44
S10	13	S9 near5 bias\$3	USPAT	OR	OFF	2007/02/07 14:44
S11	0	S10 near7 (reverse forward)	USPAT	OR	OFF	2007/02/07 14:45
S12	1	S10 same (reverse forward)	USPAT	OR	OFF	2007/02/07 14:45
S13	6273	(transistor\$1 fet\$1) near3 (reverse adj bias\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/09 15:25
S14	0	S13 near3 ((prorgammable adj logic adj device\$1) PLD\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/12 11:39
S15	39	S13 near5 ((low adj power) sleep idle standby)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/09 15:27